

L Number	Hits	Search Text	DB	Time stamp
1	575	column adj2 shift adj2 register\$1	USPAT; EPO; JPO; DERWENT	2004/08/07 17:12
2	205	(column adj2 shift adj2 register\$1) and (row adj2 shift adj2 register\$1)	USPAT; EPO; JPO; DERWENT	2004/08/07 17:13
3	65	(column adj2 shift adj2 registers) and (row adj2 shift adj2 registers)	USPAT; EPO; JPO; DERWENT	2004/08/07 17:13
4	42	(column adj2 shift adj2 register\$1) and (row adj2 shift adj2 register\$1) and (memory adj2 cell\$1)	USPAT; EPO; JPO; DERWENT	2004/08/07 17:14
5	33	(column adj2 shift adj2 register\$1) and (row adj2 shift adj2 register\$1) and (memory adj2 cells)	USPAT; EPO; JPO; DERWENT	2004/08/07 17:14
6	0	(column adj2 shift adj2 register\$1) and (row adj2 shift adj2 register\$1) and (memory adj2 cell\$1) and worline\$1 and bitline\$1	USPAT; EPO; JPO; DERWENT	2004/08/07 17:14
7	0	(column adj2 shift adj2 register\$1) and (row adj2 shift adj2 register\$1) and (memory adj2 cell\$1) and wordline\$1 and bitline\$1	USPAT; EPO; JPO; DERWENT	2004/08/07 17:14
8	12	(column adj2 shift adj2 register\$1) and (row adj2 shift adj2 register\$1) and (memory adj2 cell\$1) and ((word adj2 line\$1) or "WL" or word-line\$1)	USPAT; EPO; JPO; DERWENT	2004/08/07 17:16
9	9	(column adj2 shift adj2 register\$1) and (row adj2 shift adj2 register\$1) and (memory adj2 cell\$1) and ((word adj2 line\$1) or "WL" or word-line\$1) and ((bit-line\$1) or "BL" or bit adj2 line\$1)	USPAT; EPO; JPO; DERWENT	2004/08/07 17:17
10	12	(column adj2 shift adj2 register\$1) and (row adj2 shift adj2 register\$1) and ((word adj2 line\$1) or "WL" or word-line\$1) and ((bit-line\$1) or "BL" or bit adj2 line\$1)	USPAT; EPO; JPO; DERWENT	2004/08/07 17:17
11	11	(column adj2 shift adj2 register\$1) and (row adj2 shift adj2 register\$1) and ((word adj2 line\$1) or "WL" or word-line\$1) and ((bit-line\$1) or "BL" or bit adj2 line\$1) and select\$4	USPAT; EPO; JPO; DERWENT	2004/08/07 17:17
12	1	(column adj2 shift adj2 register\$1) and (row adj2 shift adj2 register\$1) and ((word adj2 line\$1) or "WL" or word-line\$1) and ((bit-line\$1) or "BL" or bit adj2 line\$1) and select\$4 and verif\$4	USPAT; EPO; JPO; DERWENT	2004/08/07 17:18
13	126	(shift adj2 registers) and ((word adj2 line\$1) or "WL" or word-line\$1) and ((bit-line\$1) or "BL" or bit adj2 line\$1) and select\$4	USPAT; EPO; JPO; DERWENT	2004/08/07 17:18
14	68	(shift adj2 registers) and ((word adj2 line\$1) or "WL" or word-line\$1) and ((bit-line\$1) or "BL" or bit adj2 line\$1) and select\$4 and verif\$4 and column\$1 and row\$1	USPAT; EPO; JPO; DERWENT	2004/08/07 17:27
15	14	(shift adj2 registers) and ((word adj2 line\$1) or "WL" or word-line\$1) and ((bit-line\$1) or "BL" or bit adj2 line\$1) and select\$4 and verif\$4 and column\$1 and row\$1 and ((programmable adj2 circuit\$1) or PLD)	USPAT; EPO; JPO; DERWENT	2004/08/07 17:42
16	41	(shift adj2 registers) and ((word adj2 line\$1) or "WL" or word-line\$1) and ((bit-line\$1) or "BL" or bit adj2 line\$1) and select\$4 and verif\$4 and column\$1 and row\$1 and controller	USPAT; EPO; JPO; DERWENT	2004/08/07 17:28

17	68	(shift adj2 registers) and ((word adj2 line\$1) or "WL" or word-line\$1) and ((bit-line\$1) or "BL" or bit adj2 line\$1) and select\$4 and verif\$4 and column\$1 and row\$1 and control\$3	USPAT; EPO; JPO; DERWENT	2004/08/07 17:41
18	0	(shift adj2 registers) and ((word adj2 line\$1) or "WL" or word-line\$1) and ((bit-line\$1) or "BL" or bit adj2 line\$1) and select\$4 and verif\$4 and column\$1 and row\$1 and (ISC adj2 control\$3)	USPAT; EPO; JPO; DERWENT	2004/08/07 17:37
19	294	(ISC adj2 control\$3) or (in-system adj2 configuration)	USPAT; EPO; JPO; DERWENT	2004/08/07 17:38
20	0	((ISC adj2 control\$3) or (in-system adj2 configuration)) and ((shift adj2 registers) and ((word adj2 line\$1) or "WL" or word-line\$1) and ((bit-line\$1) or "BL" or bit adj2 line\$1) and select\$4 and verif\$4)	USPAT; EPO; JPO; DERWENT	2004/08/07 17:38
21	0	((ISC adj2 control\$3) or (in-system adj2 configuration)) and (column adj2 shift adj2 register\$1)	USPAT; EPO; JPO; DERWENT	2004/08/07 17:38
22	68	(shift adj2 registers) and ((word adj2 line\$1) or "WL" or word-line\$1) and ((bit-line\$1) or "BL" or bit adj2 line\$1) and (select\$4 or multiplx\$4 or MUX) and verif\$4 and column\$1 and row\$1 and control\$3	USPAT; EPO; JPO; DERWENT	2004/08/07 17:41
23	14	(shift adj2 registers) and ((word adj2 line\$1) or "WL" or word-line\$1) and ((bit-line\$1) or "BL" or bit adj2 line\$1) and (select\$4 or MUX or multiplx\$4) and verif\$4 and column\$1 and row\$1 and ((programmable adj2 circuit\$1) or PLD)	USPAT; EPO; JPO; DERWENT	2004/08/07 17:44
24	51	(shift adj2 registers) and ((word adj2 line\$1) or "WL" or word-line\$1) and ((bit-line\$1) or "BL" or bit adj2 line\$1) and (select\$4 or MUX or multiplx\$4) and ((programmable adj2 circuit\$1) or PLD)	USPAT; EPO; JPO; DERWENT	2004/08/07 18:01
25	0	weing-d\$.in.	USPAT; EPO; JPO; DERWENT	2004/08/07 18:02
26	0	weiyng-d\$.in.	USPAT; EPO; JPO; DERWENT	2004/08/07 18:02
28	9	vest-b\$.in.	USPAT; EPO; JPO; DERWENT	2004/08/07 18:04
27	148	ding-w\$.in.	USPAT; EPO; JPO; DERWENT	2004/08/07 18:03
29	0	ding-weiyng.in.	USPAT; EPO; JPO; DERWENT	2004/08/07 18:03
30	19	ding-wei\$.in.	USPAT; EPO; JPO; DERWENT	2004/08/07 18:04
31	0	vest-brad.in.	USPAT; EPO; JPO; DERWENT	2004/08/07 18:04